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APPLICANTS

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**** CONTINUING DATA *******
 This application is a CON of 10/179,222 06/26/2002 PAT 6,703,891
 which is a CON of 09/565,116 05/04/2000 PAT 6,456,152 *TDC*

**** FOREIGN APPLICATIONS *******
 JAPAN 11-135557 05/17/1999 *TDC*

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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>TDC</i> Examiner's Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 12	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 3
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TITLE
 Semiconductor integrated circuit device

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